

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Cheon Soo KIM, et al.

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: September 29, 2003 Examiner: Not Yet Assigned

Title: STACKED VARIABLE INDUCTOR

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 29, 2003

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P69148US0 GROUP ART UNIT: Not Yet Assigned
 SERIAL NO.: New Application FILING DATE: September 29, 2003
 APPLICANT(S): Cheon Soo KIM, et al. TODAY'S DATE: September 29, 2003

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
AA	5,461,353	10/24/95	Eberhardt	H01F	21/12	08/30/94
AB	6,201,287	03/13/01	Forbes	H01L	29/00	10/26/98
AC	4,841,253	06/20/89	Crabill	H03F	3/60	04/15/87

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AD EP 0694904 A2	01/31/96	Europe	G10L	5/04	X
AE 2000-15617	03/15/00	Korea	H01F	21/00	X
AF 8-162331	06/21/96	Japan	H01F	21/12	X

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AG Alireza Zolfaghari, et al.; "Stacked Inductors and Transformers in CMOS Technology"; IEEE JOURNAL OF SOLID-STATE CIRCUITS; Vol. 36, No. 4; April 2001; pp. 620-628.

AH Chih-Chun Tang, et al.; "Miniature 3-D Inductors in Standard CMOS Process"; IEEE JOURNAL OF SOLID-STATE CIRCUITS; Vol. 37, No. 4; April 2002; pp. 471-480.

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).